

ECAL

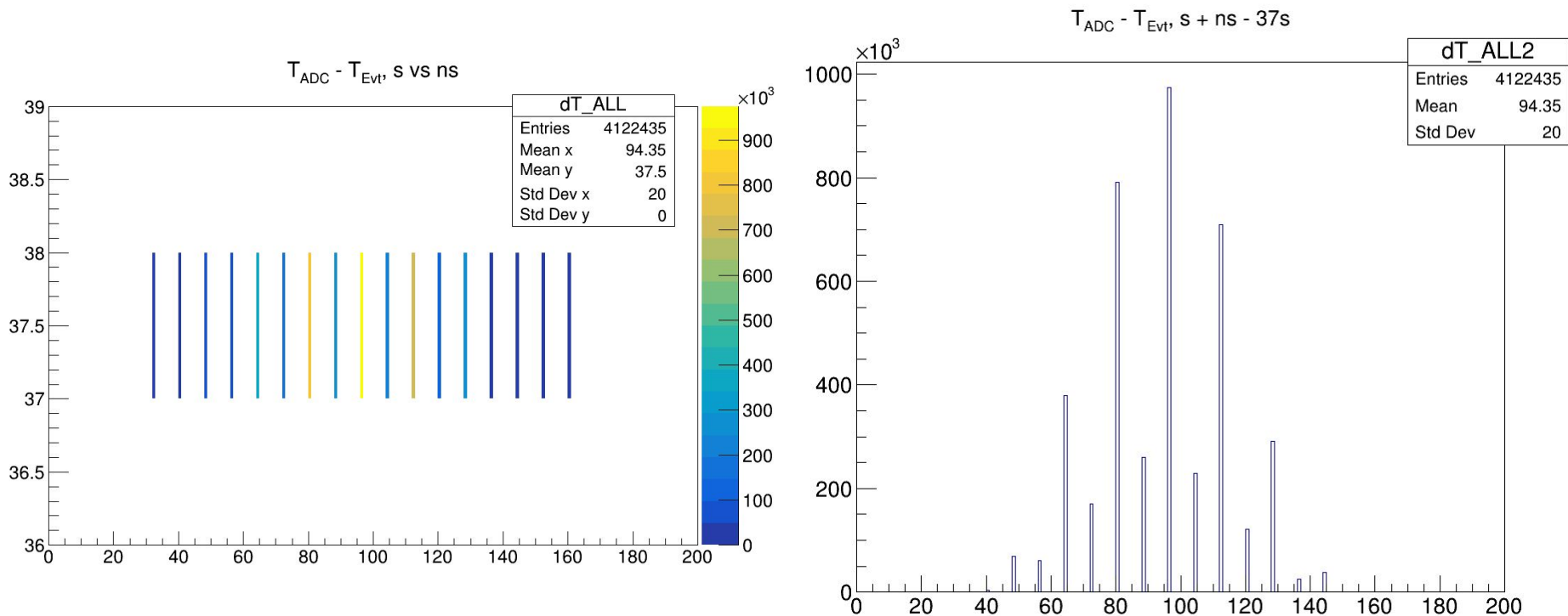
25.01.2021

BmnRawDataDecoder modification to store ADC timestamps

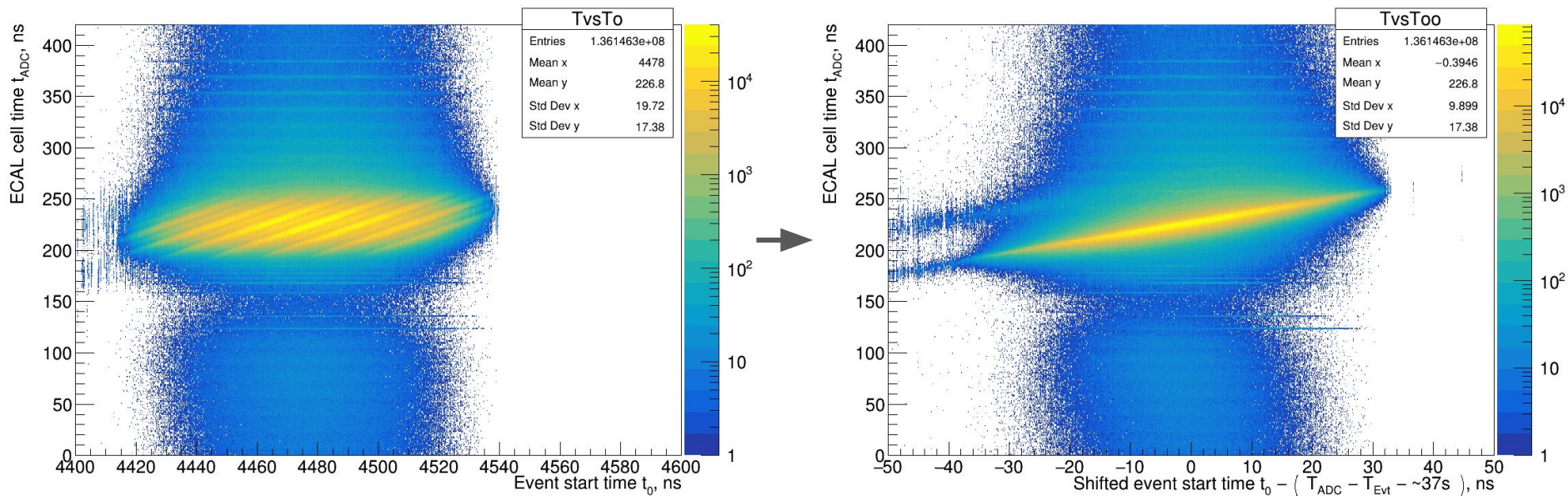
```
BmnRawDataDecoder.cxx
Источник History
588 while (i < len) {
589     while (i < len) {
590         UInt_t subType = d[i] & 0x3;
591         if (subType == 0) {
592             i += 5; //skip unused words
593             UInt_t iCh = 0;
594             while (iCh < kNCH - 1 && i < len) {
595                 iCh = d[i] >> 24;
596                 ns = (d[i] & 0xFFFF) / 2 - 4;
597                 // printf("WR serial %08X ns = %d\n", serial, ns);
598                 Int_t sec = d[i+1];
599                 Int_t nsec = d[i+2];
600                 i += 3; // skip two timestamp words (they are empty)
601                 for (Int_t iWord = 0; iWord < ns / 2; ++iWord) {
602                     val[2 * iWord + 1] = d[i + iWord] & 0xFFFF; //take 16 lower bits and put them into corre
603                     val[2 * iWord] = (d[i + iWord] >> 16) & 0xFFFF; //take 16 higher bits and put them into
604                 }
605
606                 TClonesArray& ar_adc = *arr;
607                 if (iCh >= 0 && iCh < kNCH) {
608                     // printf("ns == %d, serial == 0x%0x, chan == %d\n", ns, serial, iCh);
609                     BmnADCData * p = new(ar_adc[arr->GetEntriesFast()]) BmnADCData(serial, iCh, ns, val);
610                     p->GetTimeStamp()->SetSec(sec);
611                     p->GetTimeStamp()->SetNanoSec(nsec);
612                     p->Print();
613                 }
614                 i += (ns / 2); //skip words (we've processed them)
615             }
616         } else break;
617     }
618     return kBMSUCCESS;
619 }
```

- BmnADCData class is a modification of BmnADCDigit class with timestamp added
- All these are my local changes, not in the repository

Timestamps difference between ECAL and Event header

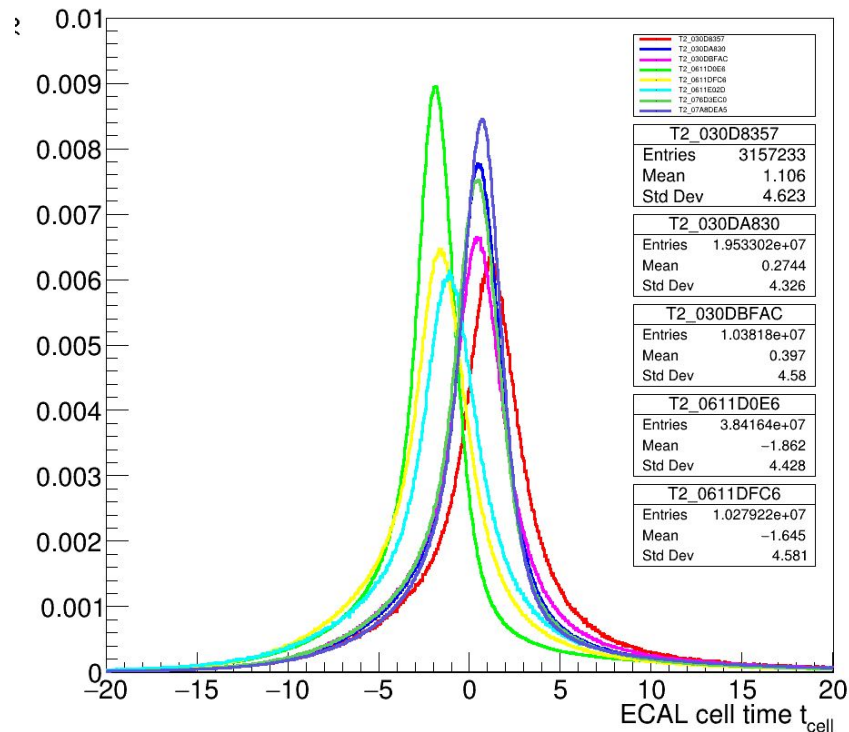
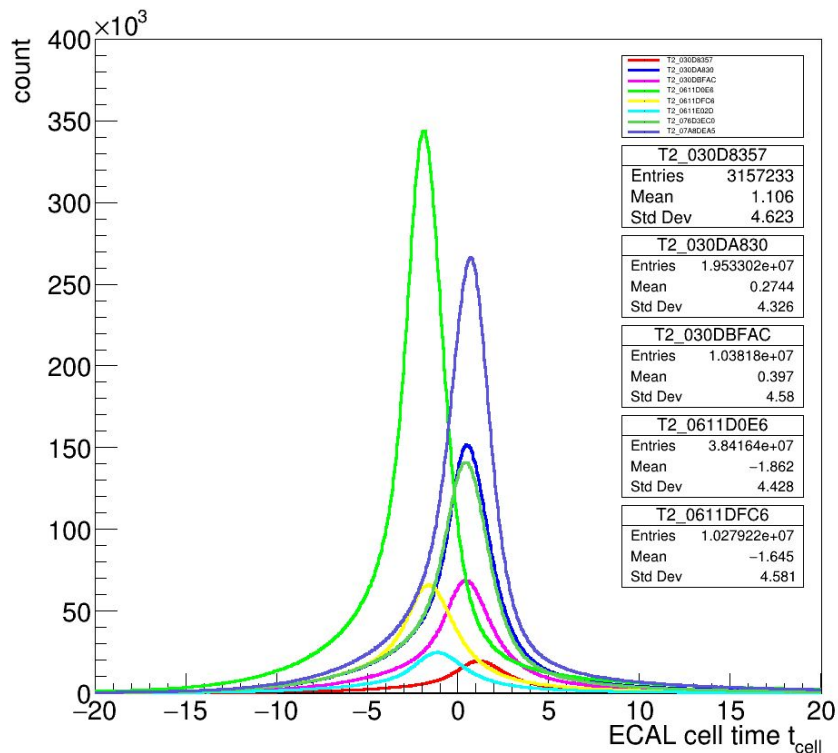


Shift event start time t_0 by timestamps difference

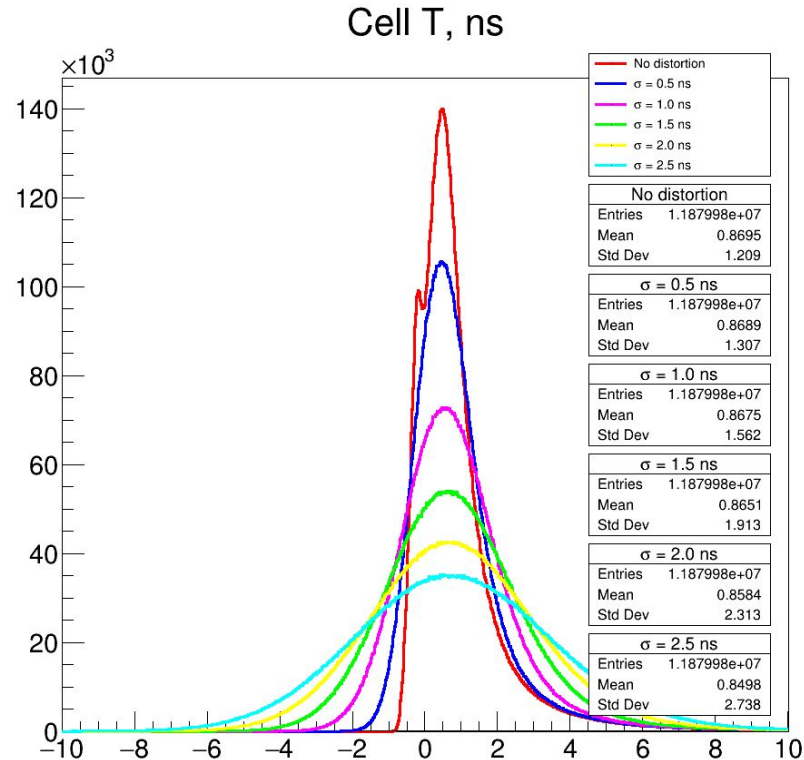
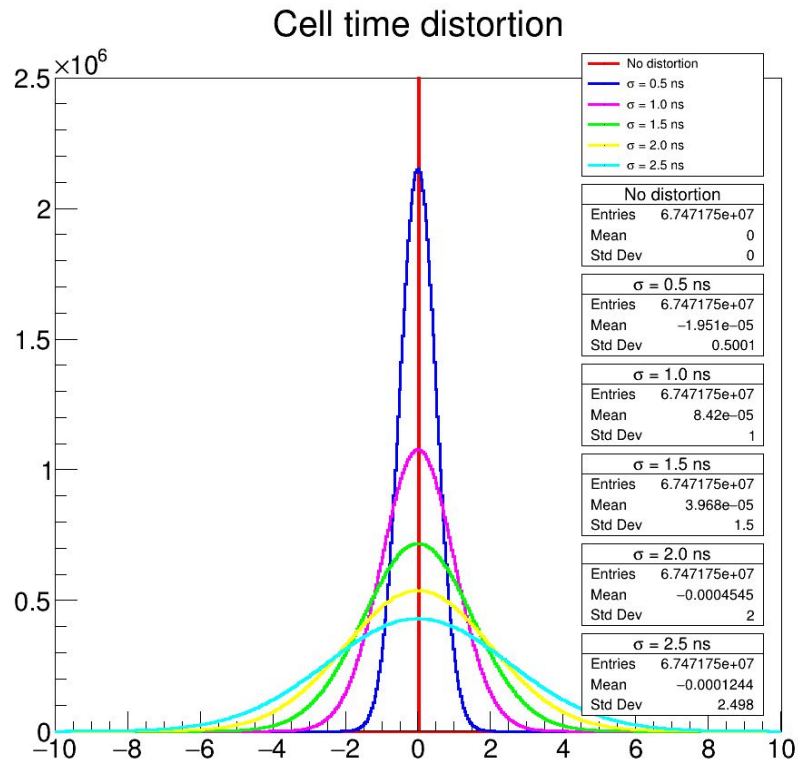


$\sim 37s$ is 37s and 4384ns

Cell time corrected by shifted event start time grouped by ADC board address

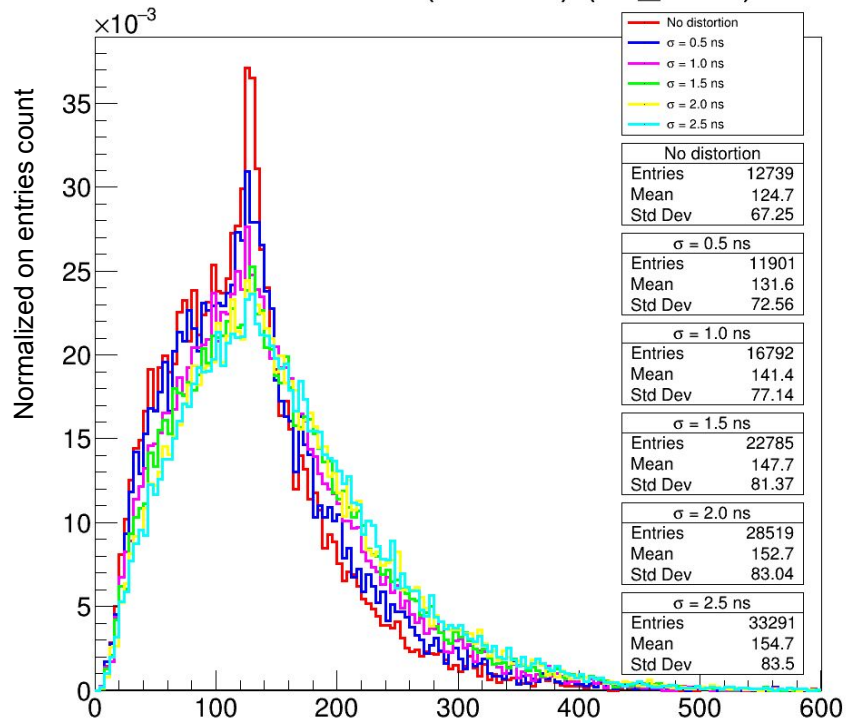


Distortion of the cells signal time of the ECAL in MC

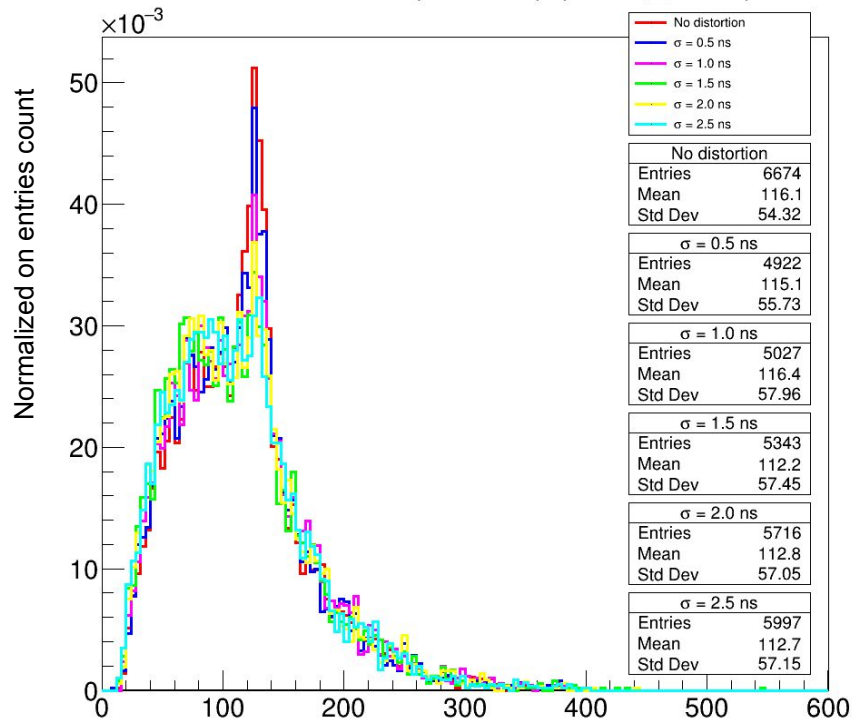


Distortion of the cells signal time of the ECAL in MC

Effective mass (internal) (All_TCut)



Effective mass (internal) (mCut_TCut)



ZDC

25.01.2021

Run set **Ar** → **Al**

4421 - Test run for T0

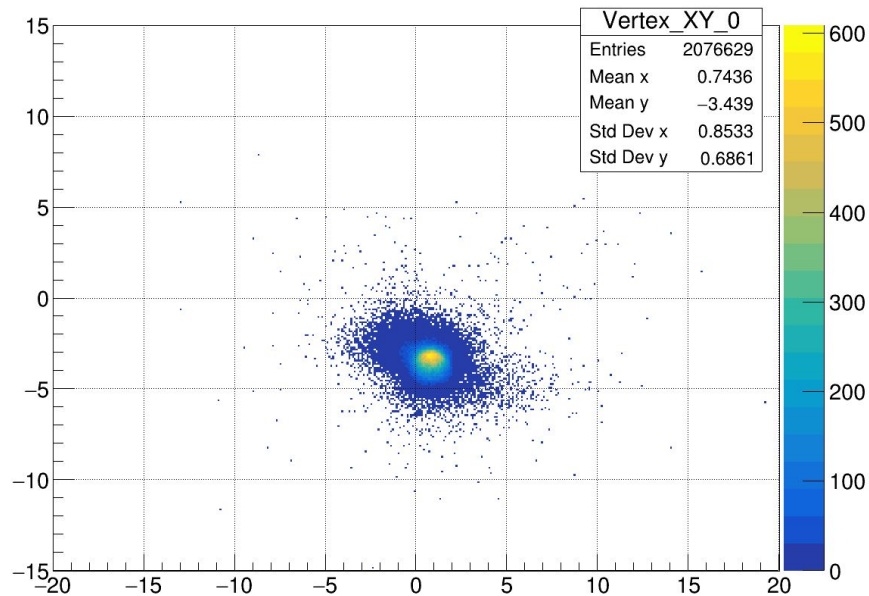
- Beam Trigger (BC1+BC2+T0+Veto)
- SP-41: 1250 A
- SP-57: 10 A
- VKM2: 240 A
- Beam: Ar 3.2 AGeV
- Target: Al (3.33 mm)

4519, 4520, 4521, 4522, 4524, 4525,
4649, 4651, 4652, 4653, 4654

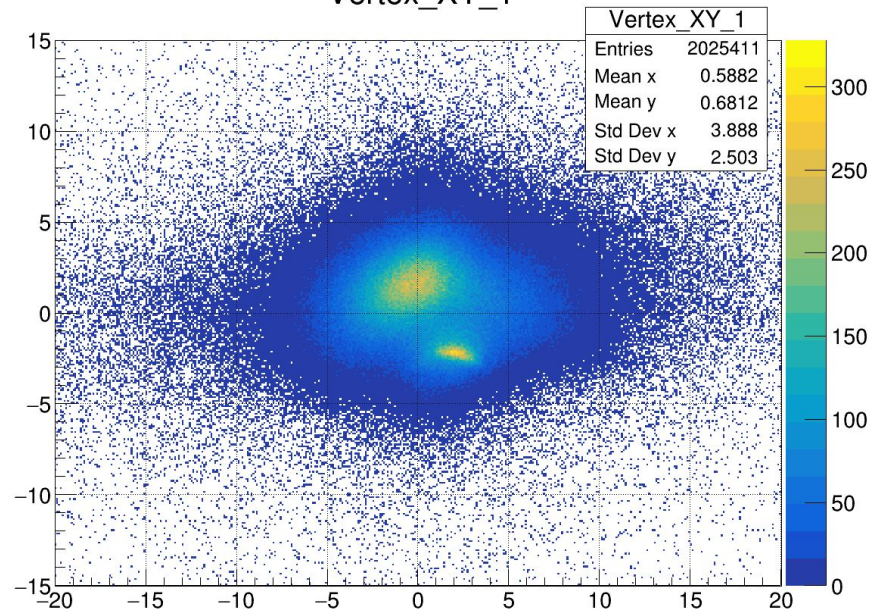
- Beam Trigger + BD(>1) and Si(>2)
- SP-41: 1250 A
- SP-57: 0 A
- VKM2: 160 A, 140 A
- Beam: Ar 3.2 AGeV
- Target: Al (3.3 mm)
- ~2M events

Vertex

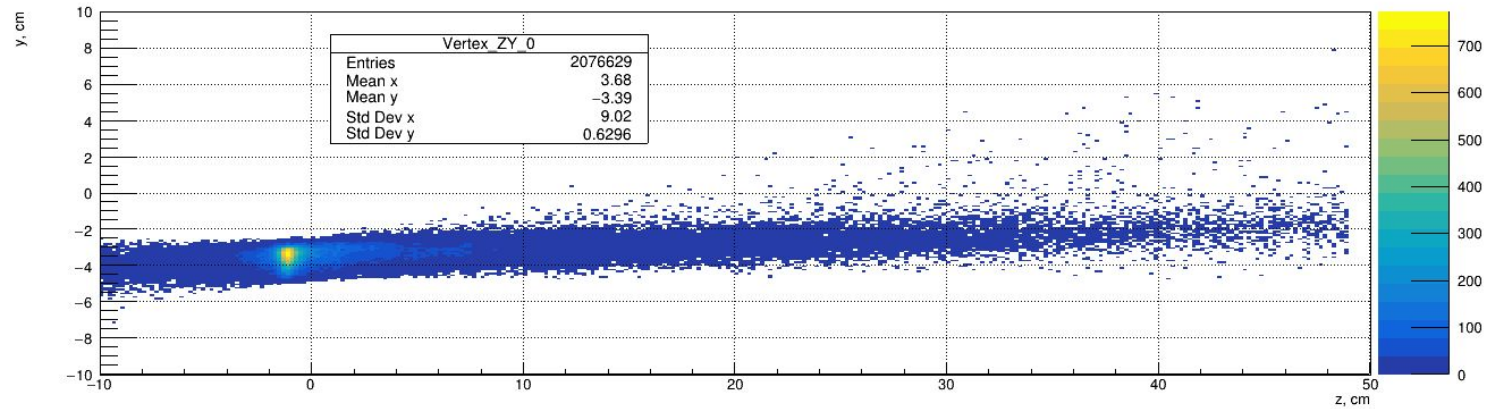
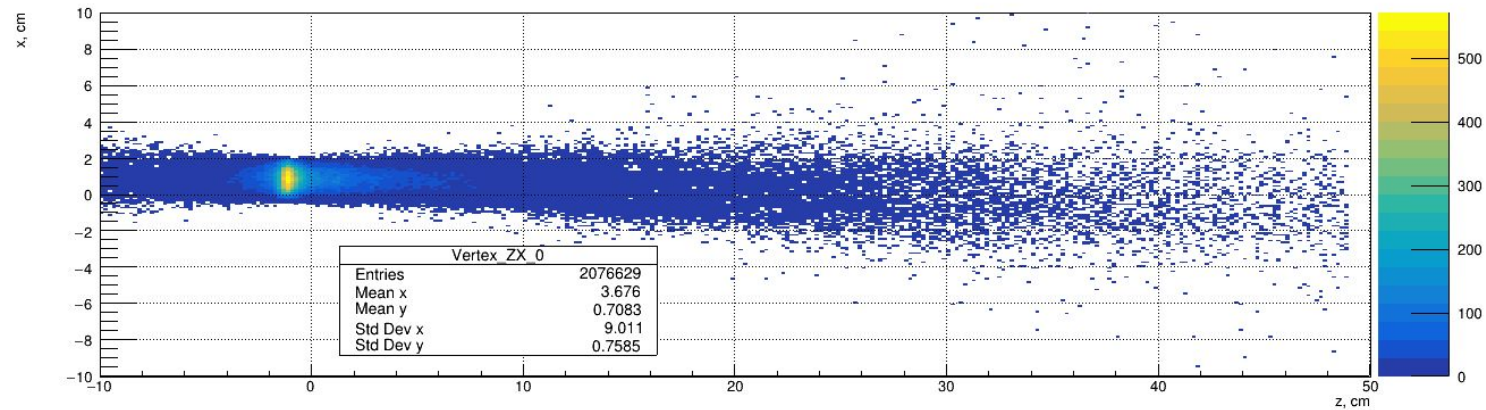
Vertex_XY_0



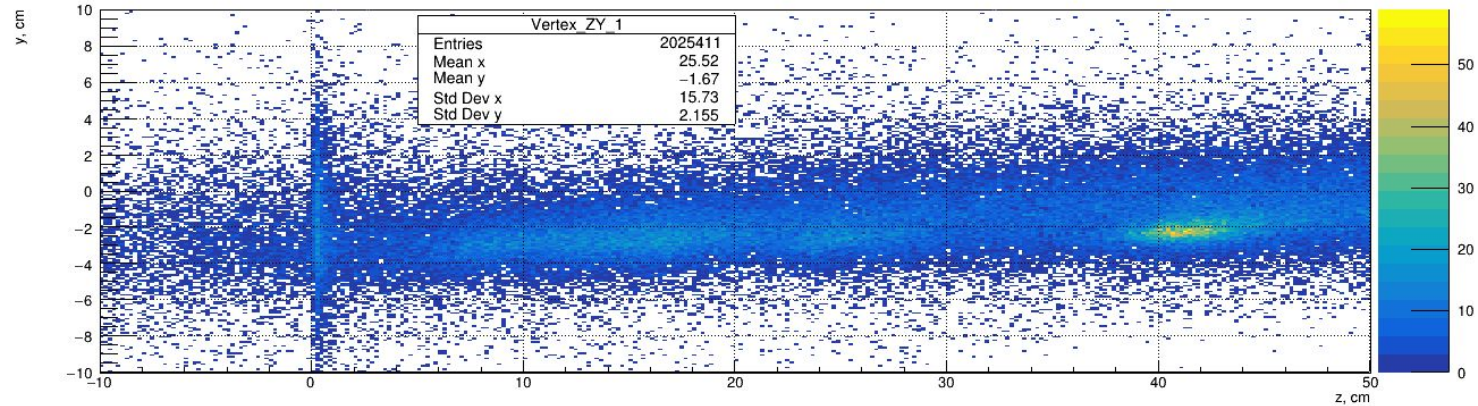
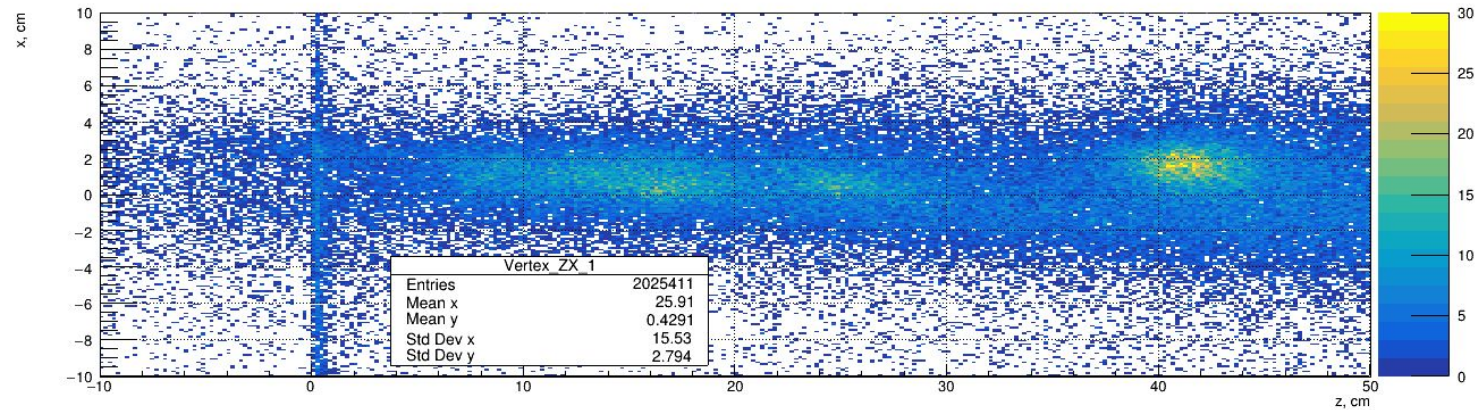
Vertex_XY_1



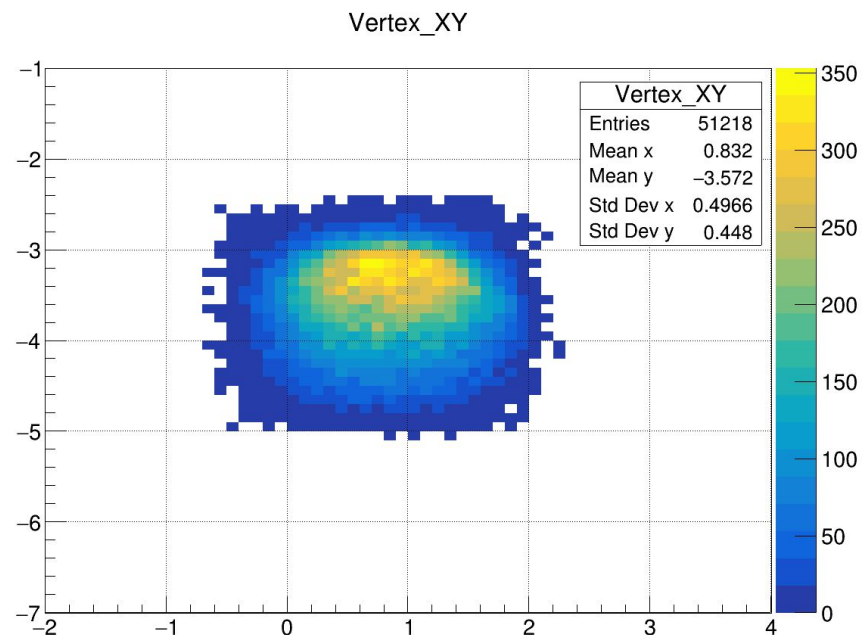
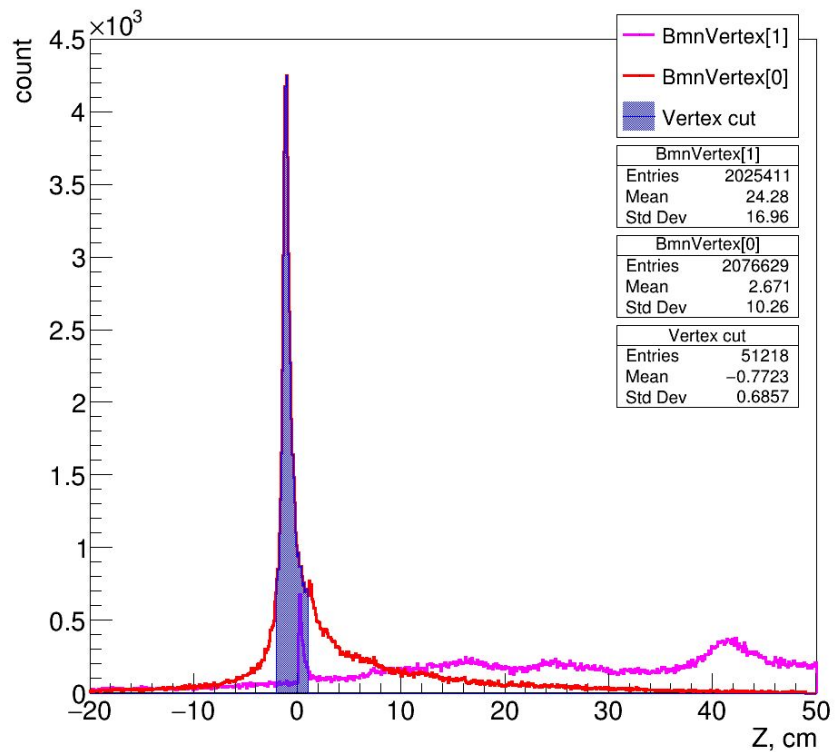
BmnVertex[0]

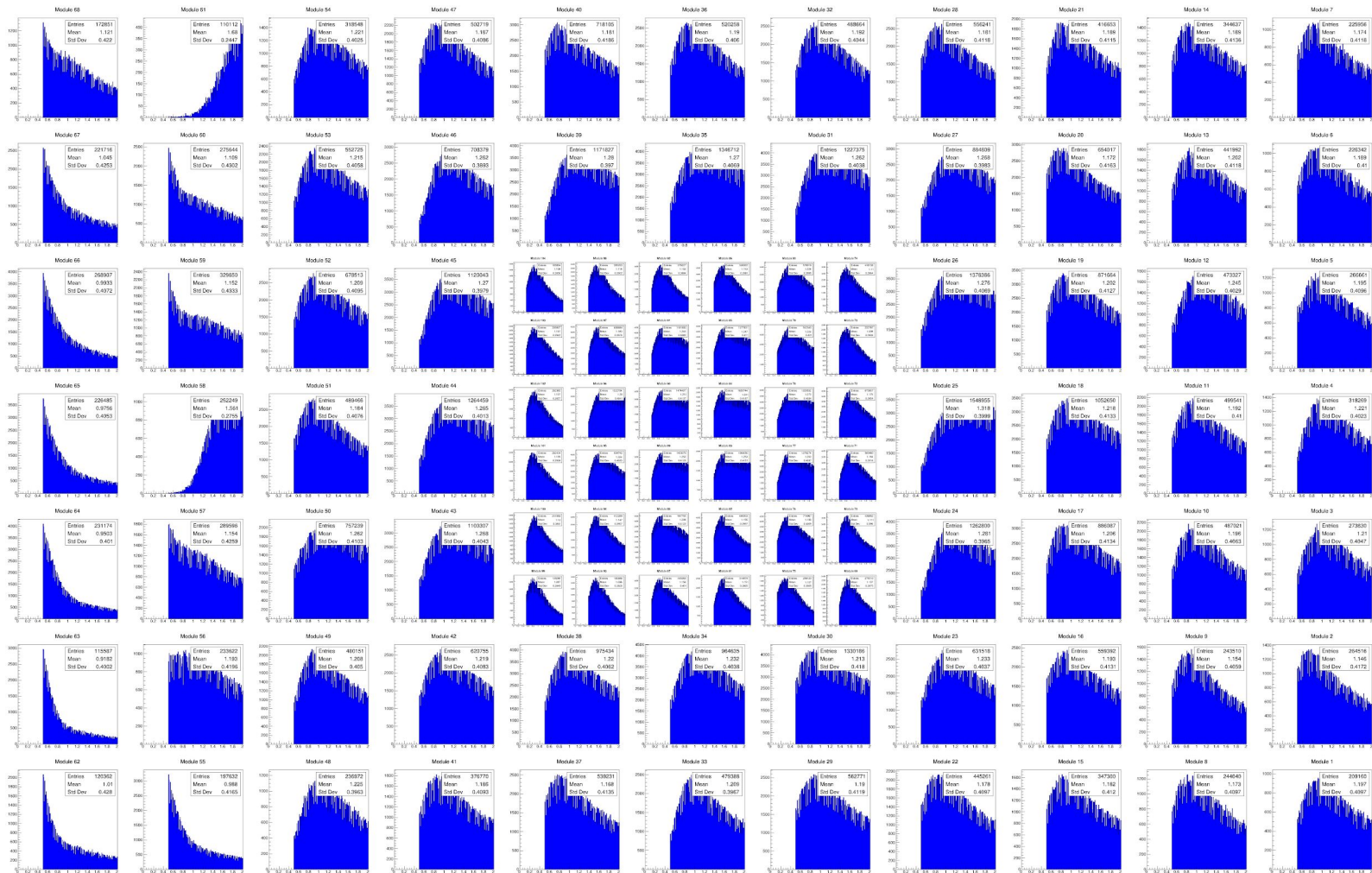


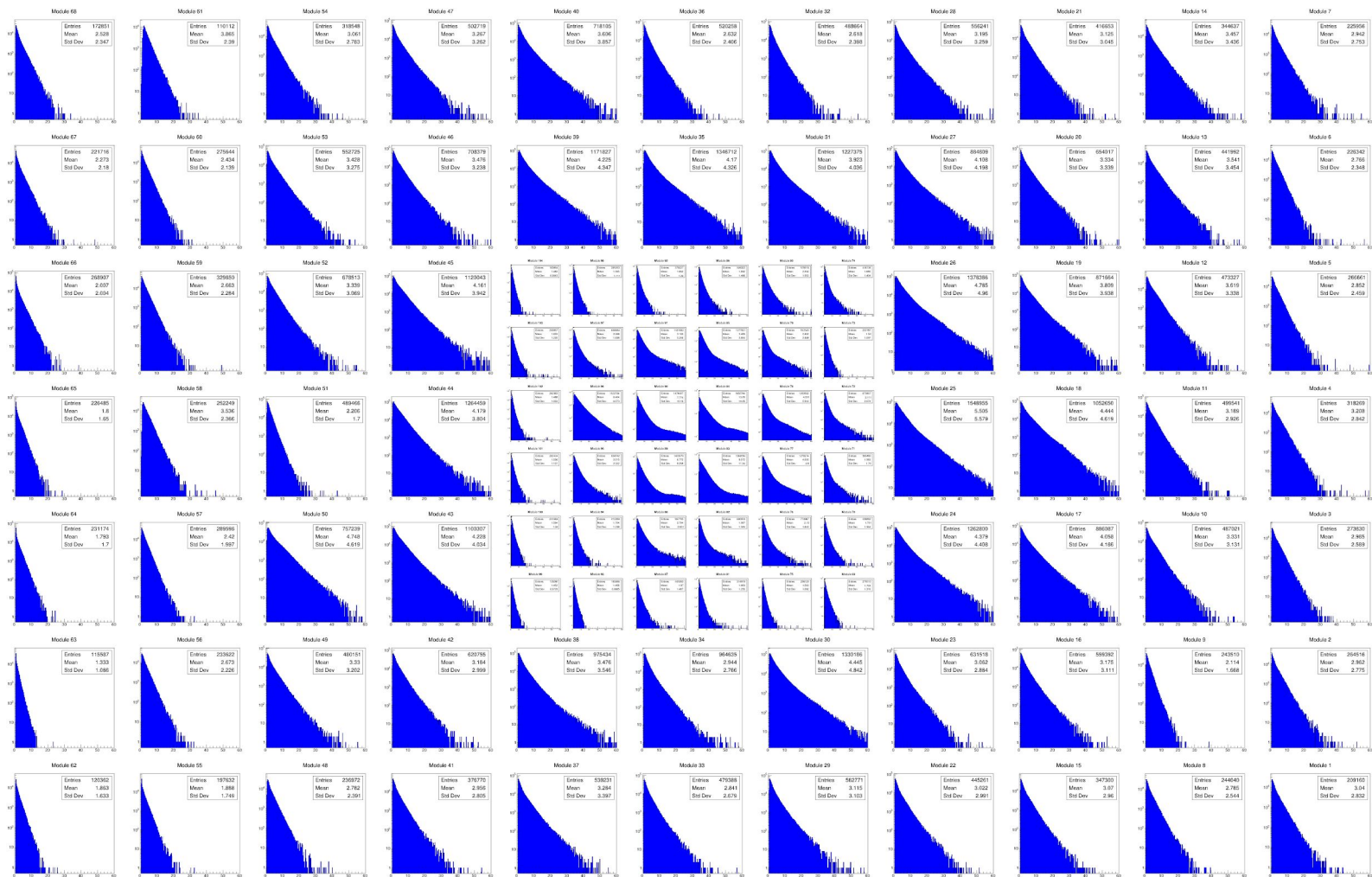
BmnVertex[1]



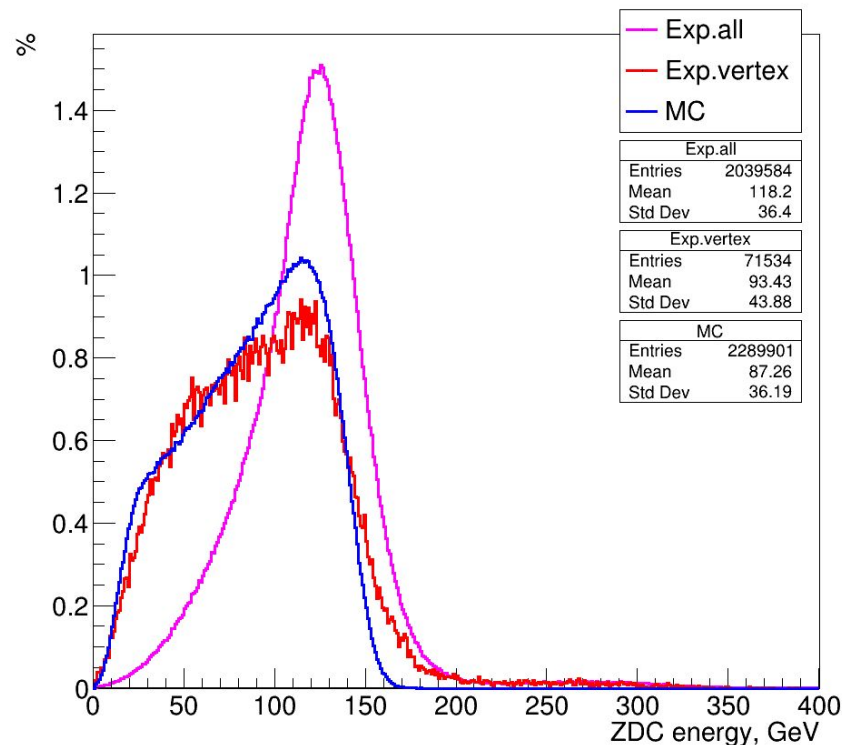
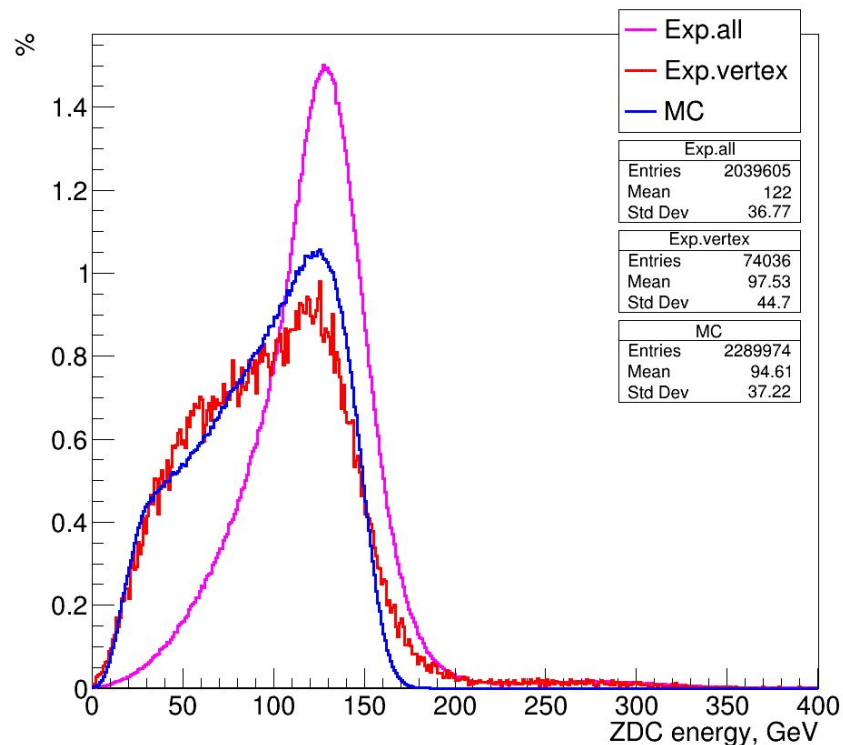
Vertex cut: $-2 \text{ cm} < z < 1 \text{ cm}$



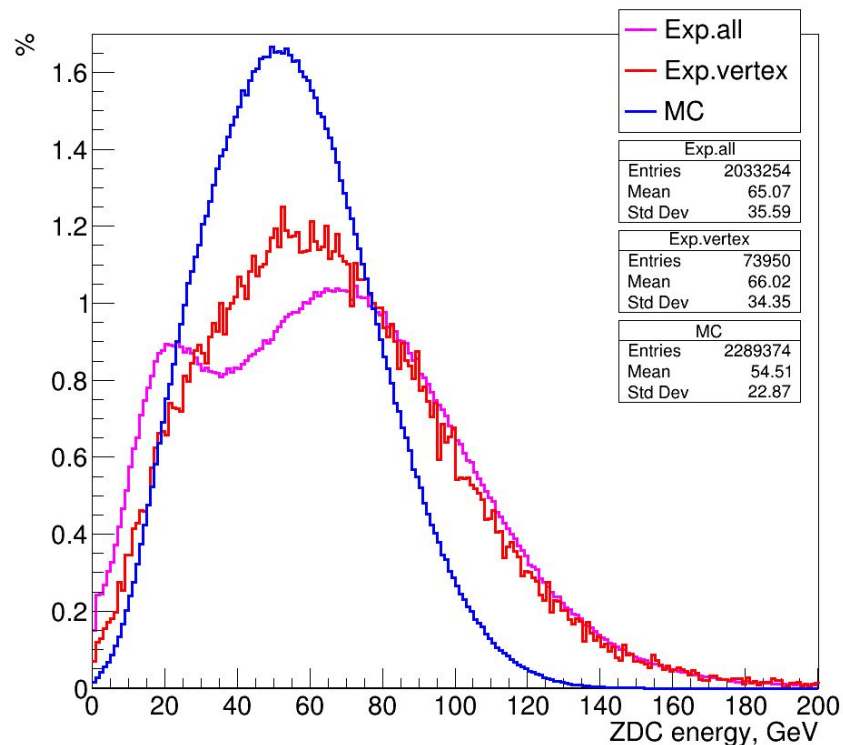




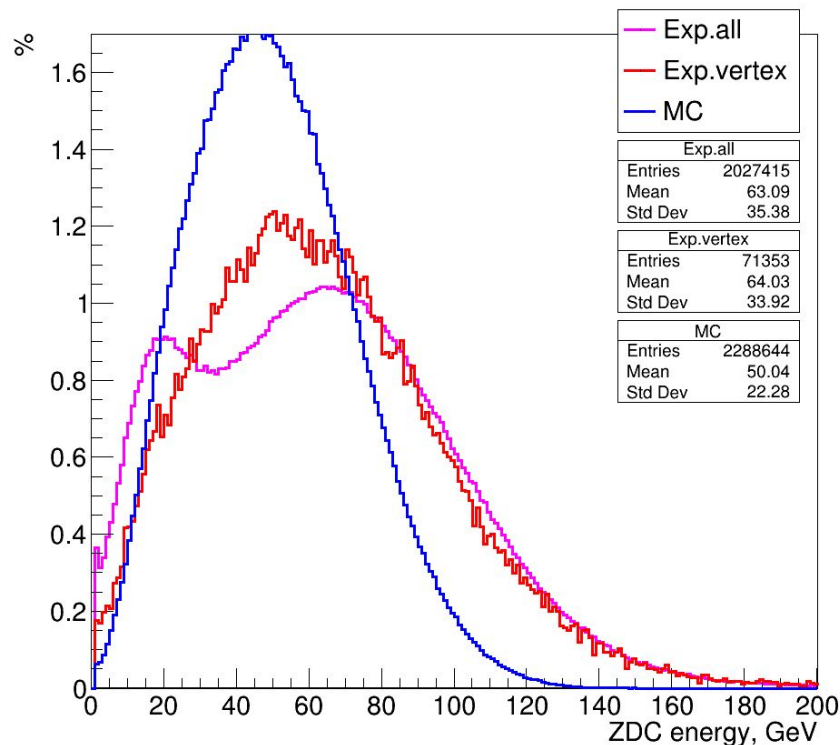
ZDC total energy (all modules)



ZDC energy (large modules only)

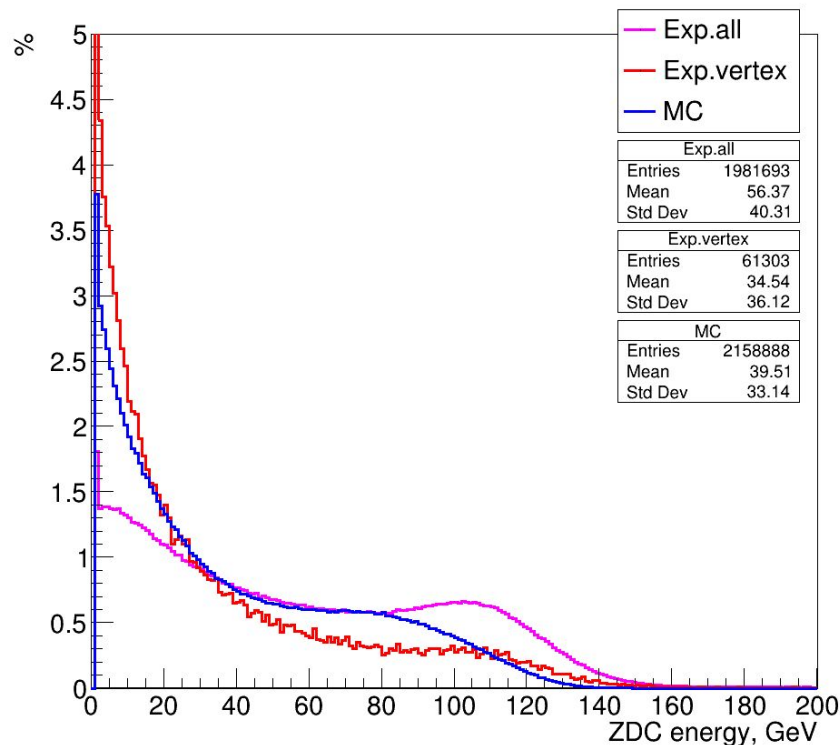
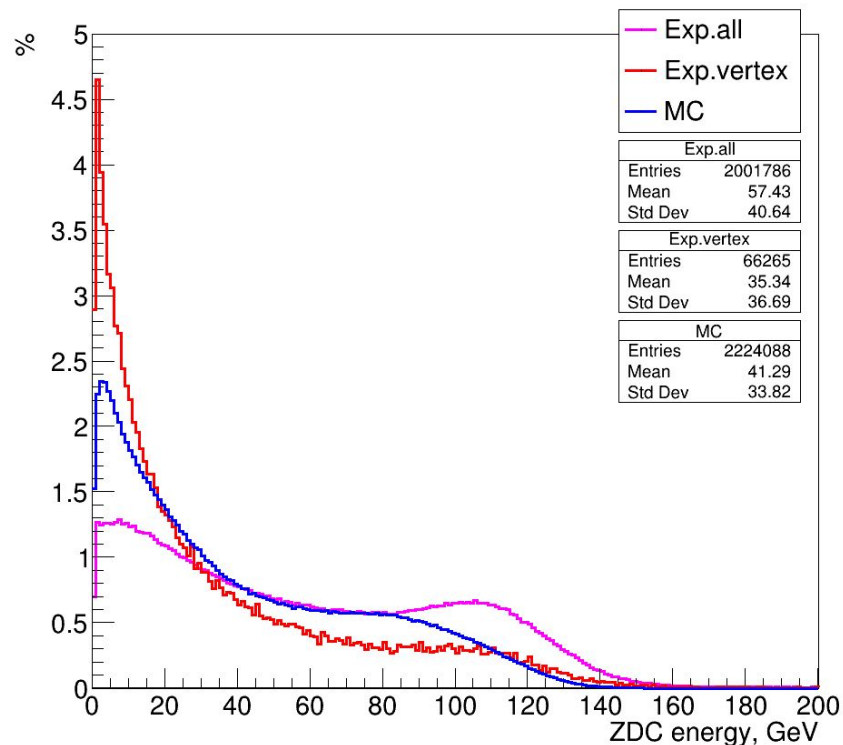


threshold 0.5 GeV

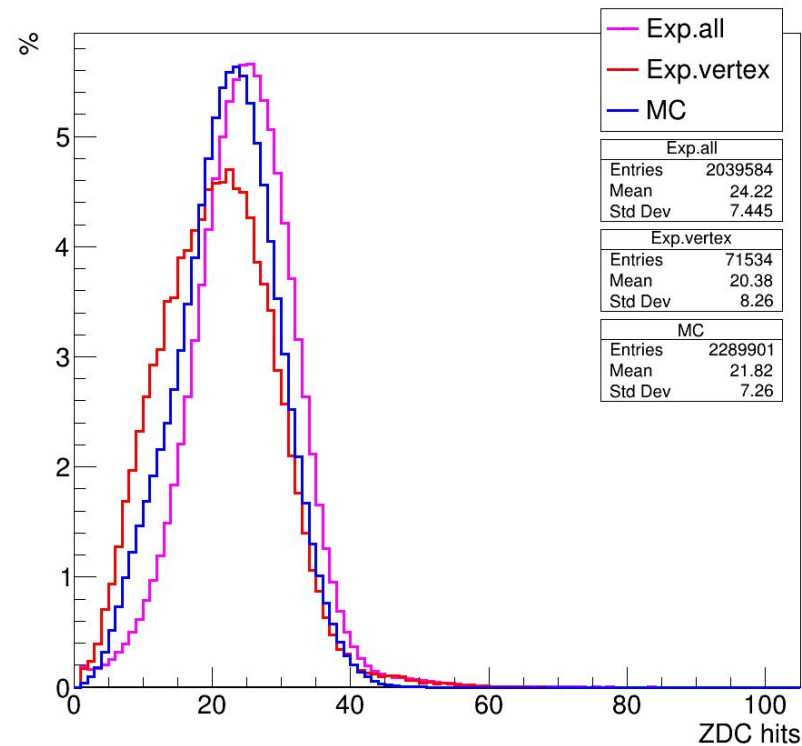
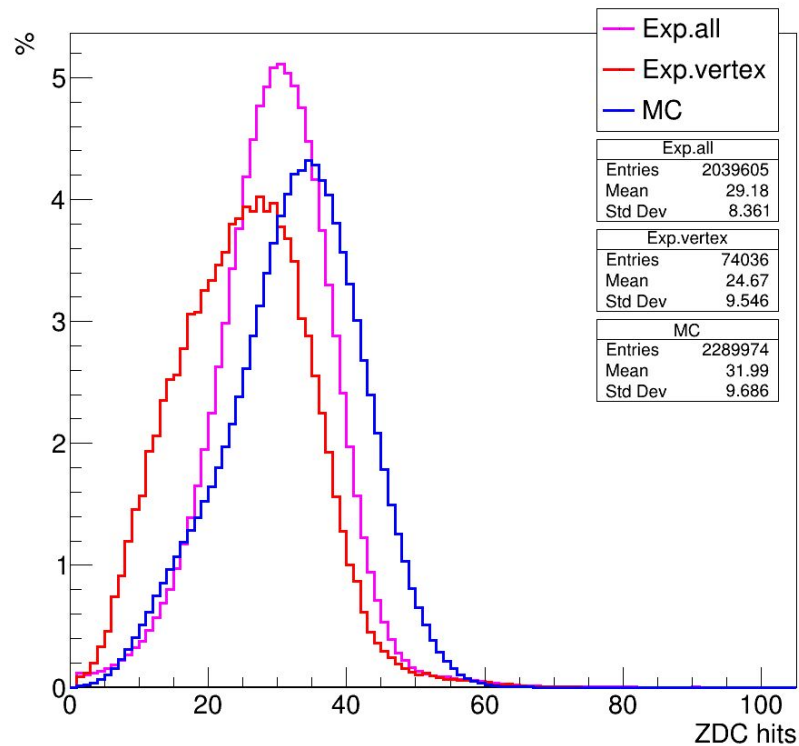


threshold 1 GeV

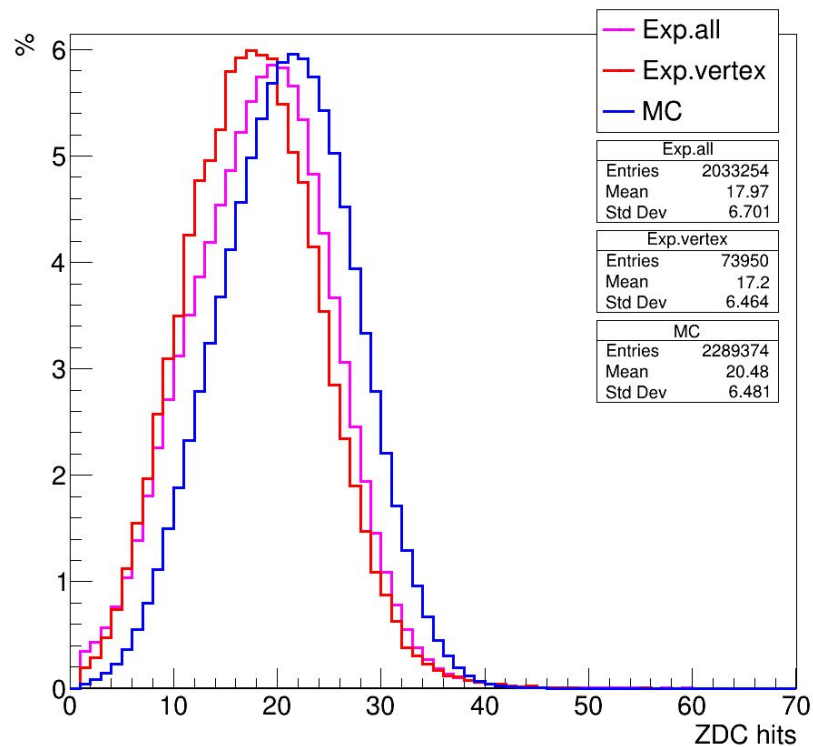
ZDC energy (small modules only)



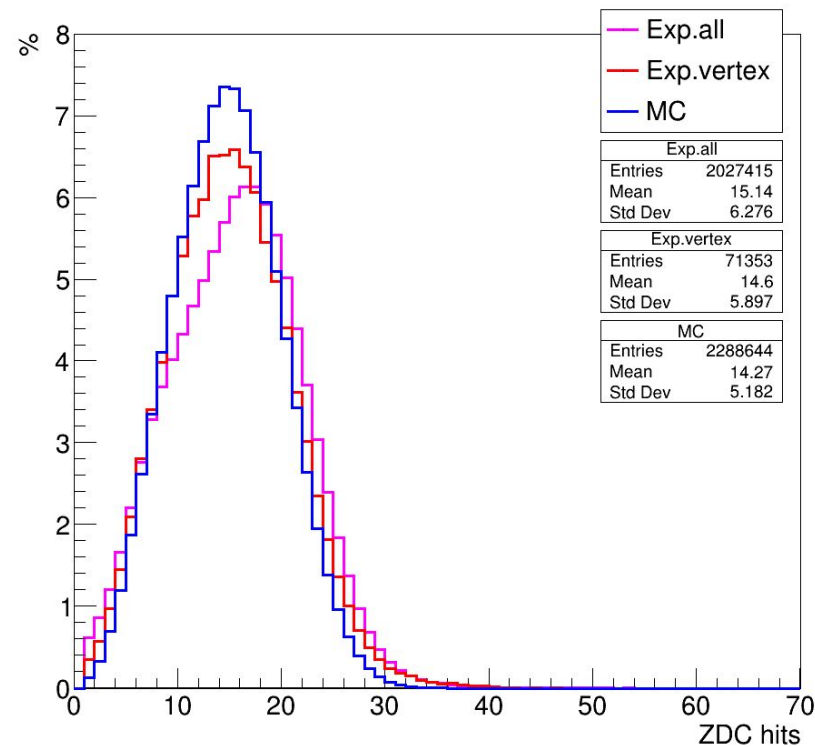
ZDC multiplicity (all modules)



ZDC multiplicity (large modules only)

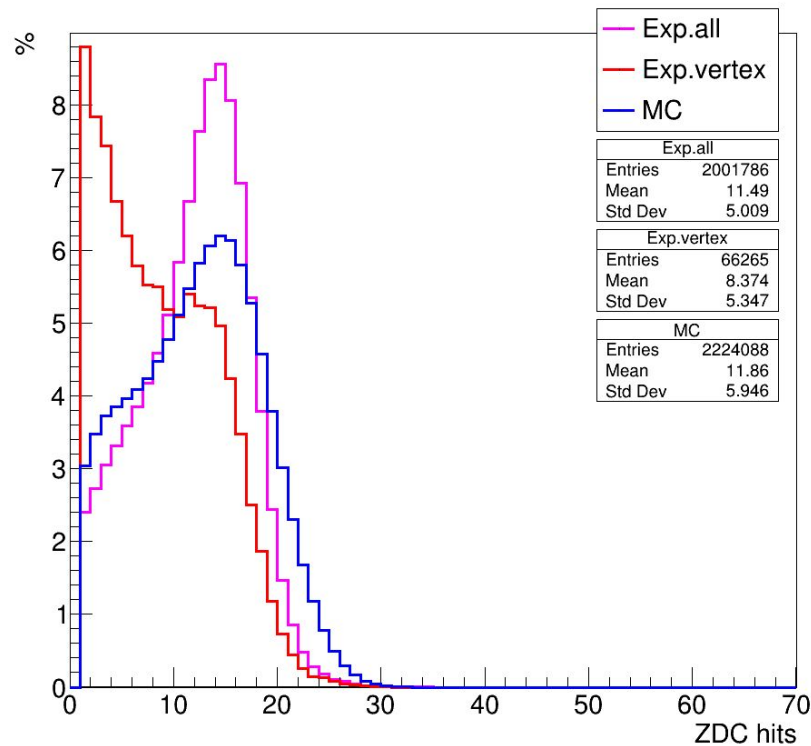


threshold 0.5 GeV

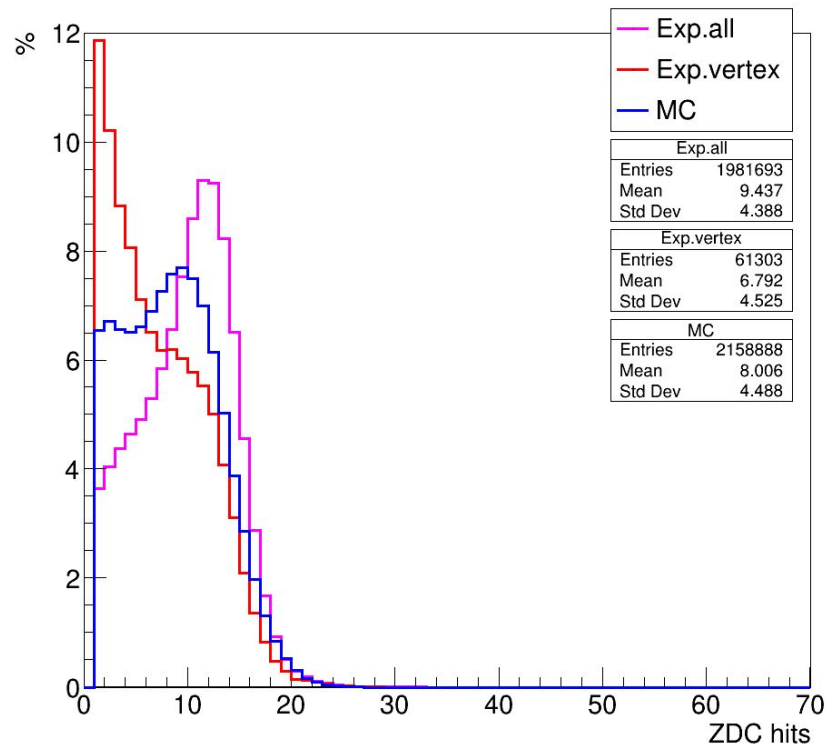


threshold 1 GeV

ZDC multiplicity (small modules only)

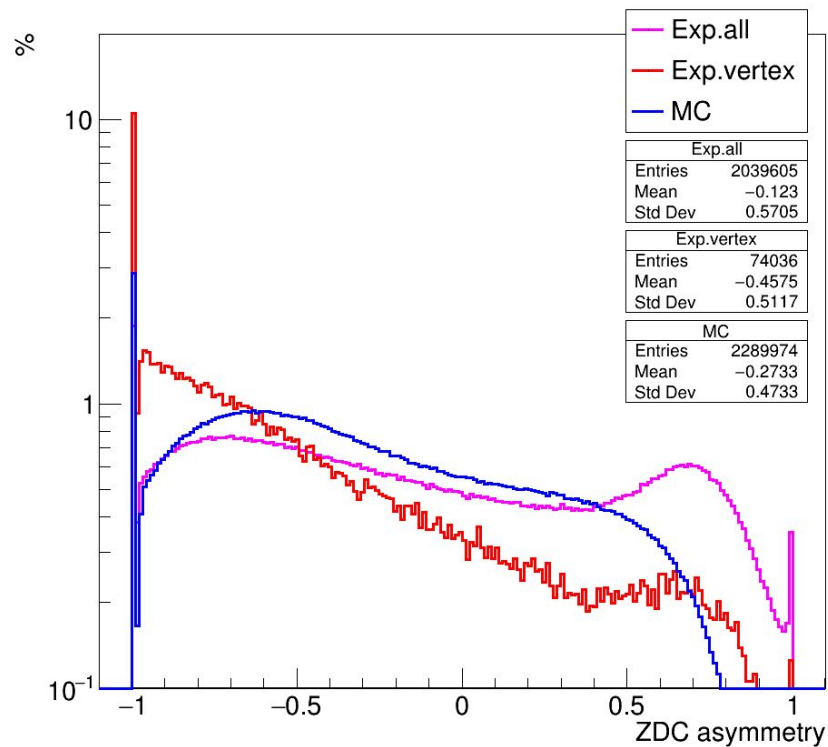


threshold 0.5 GeV

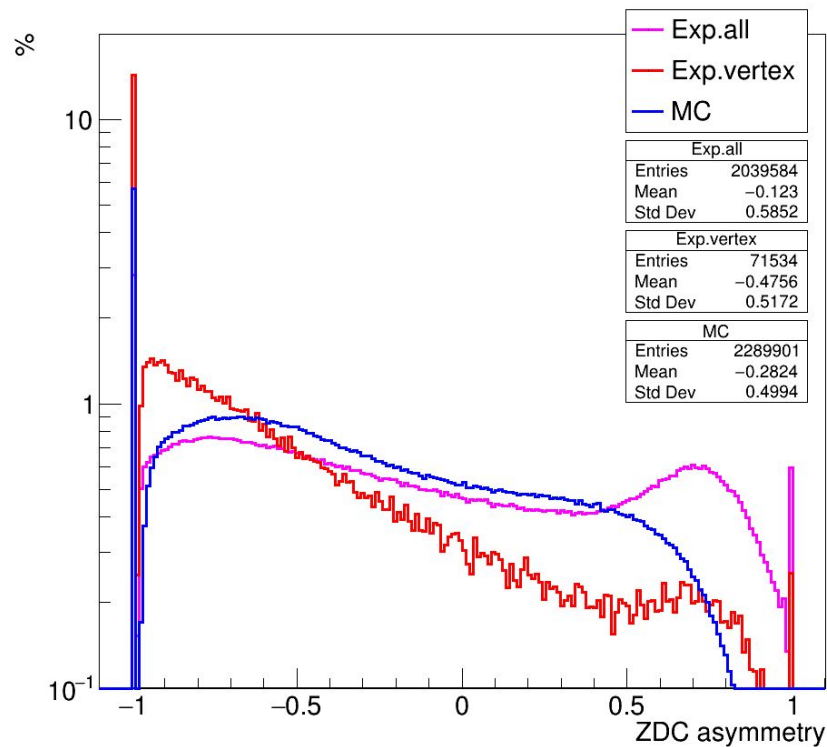


threshold 1 GeV

ZDC multiplicity (small modules only)

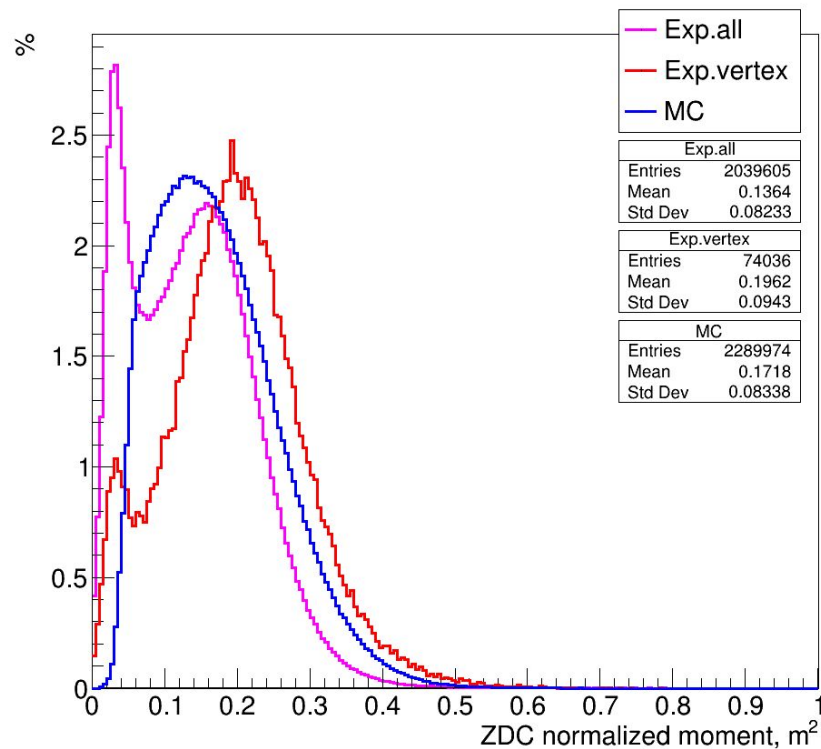


threshold 0.5 GeV

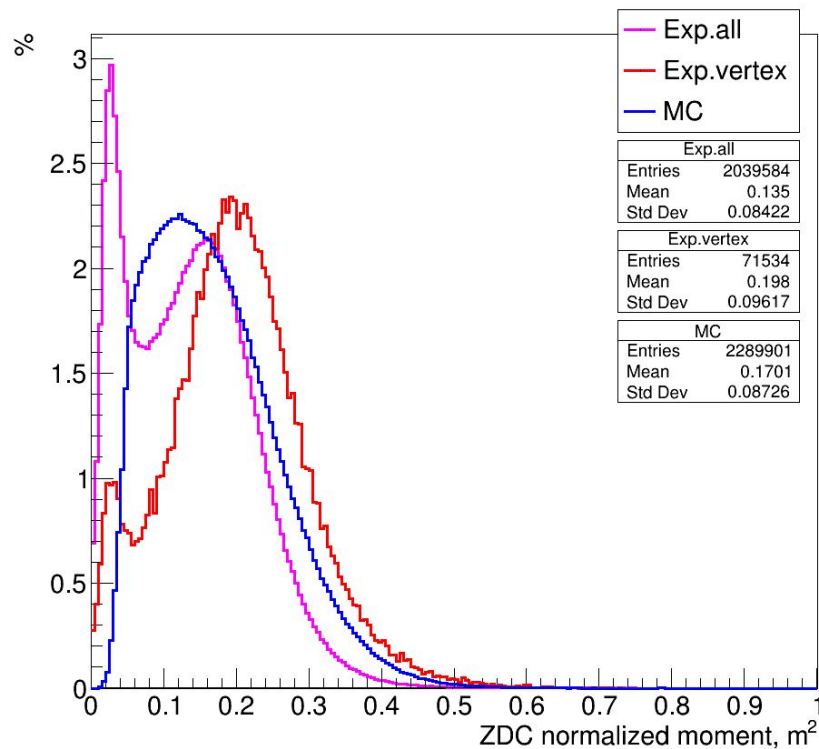


threshold 1 GeV

ZDC multiplicity (small modules only)



threshold 0.5 GeV



threshold 1 GeV