

Feynman Integral evaluation and reduction at supercomputers

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During the last years the CPU speed is not growing much, and parallization with the use of shared memory is widely used but also has it limits. Still there are new reduction and evaluation tasks and new approaches are needed. So how can the supercomputers be used in order to help researcher with these tasks?

In this talk we are going to disscuss an already existing modification of FIESTA that makes it possible to apply it at supercomputers, as well as an approach that leads to the possiblity to perform Feynman integral reduction with the use of supercomputers.

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