

Session Program

28 September 2015 to 2 October 2015



Montenegro, Budva, Becici, 28 september - 02 october 2015

Computing for Large Scale Accelerator Facilities (LHC, FAIR, NICA, etc.) and Big Data

Budva, Becici, Hotel Splendid, Conference Hall

Friday 2 October

09:00

Computing for Large Scale Accelerator Facilities (LHC, FAIR, NICA, etc.) and Big Data

Session | Location: Budva, Becici, Hotel Splendid, Conference Hall | Convener: Ms Julia Andreeva

09:00–09:30 ALFA: Next generation concurrent framework for ALICE and FAIR experiments

Speaker

Dr Mohammad Al-Turany

09:30–10:00 Data analytics in the ATLAS Distributed Computing

Speaker

Dr Ilija Vukotic

10:00–10:30 Integration Of PanDA Workload Management System With Supercomputers

Speaker

Dr Alexei Klimentov

10:30–10:50 The Next Generation ATLAS Production System

Speaker

Mr Mikhail Borodin

10:50

11:10

Computing for Large Scale Accelerator Facilities (LHC, FAIR, NICA, etc.) and Big Data

Session | Location: Budva, Becici, Hotel Splendid, Conference Hall | Convener: Prof. Dario Barberis

11:10–11:40 dCache, Sync-and-Share for Big Data

Speaker

Dr Patrick Fuhrmann

11:40–12:10 Complex for mega-science data modeling and processing

Speaker

Eygene Ryabinkin

12:10–12:40 Big Data processing: test results

Speaker

Prof. Alexander Degtyarev

12:40