

The Scientific Conference of Young Scientists and Specialists



# Performance of a new fast timing generation of RPC detector of the high eta CMS muon stations

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## Outline

### **Motivation and Goals**

### • Motivation and Goals of Research & Development

- CERN CMS Resistive Plate Chamber (RPC) upgrade project
- Scheme of Resistive Plate Chambe and Readout system

### **Protypype of RPC**

### • Improve RPC (iRPC): RETURN and COAX prototypes

- RETURN and COAX prototypes
- Front-End Board with Petiroc 2A
- Printed Circuit Board (strips)

### • Electronic PETIROC2A: Pedestal, Injection, Noise

- Pedestal alignment
- Calibration with injection signal form generator

### • Description of the stand for tests of the prototype

- Scintilators setup
- Raw data profiles
- The noise of the prototype

### **Test of RPC**

### • H2 line: Study of time resolution

• Stand Description, Results

### • GIF++: Study of rate capability

• Stand Description, Results

# **Motivation and Goals of Research & Development**

#### High η CMS RPC upgrade project

#### RE3/1-RE4/1 muon stations motivation:

- To improve on the muon detector performance.
- Heavy Stable Charged Particle search.
- To improve on the muon trigger efficiency at high η

 $1.8 < |\eta| < 2.4$ 

• Detectors should be able to withstand high particle rates:  $2 kHz \cdot cm^{-2}$ 



Layout of one quadrant of CMS. The slots RE3/1 and RE4/1 are to be instrumented by RPC chambers for HL-LHC upgrade [1]



**Resistive Plate Chamber (RPC) and Readout** 

Thinner gap in the double gap RPC detector [2] 95.2% C2H2F4, 4.5% i-C4H10, and 0.3% SF6 lower charge  $\rightarrow$  less aging  $\rightarrow$  needs more sensitive electronics higher rate  $\rightarrow$  more combinatory  $\rightarrow$  needs better space resolution

Standard Readout

Time of Arrival (Proposal Solution)

Determine position along a strip of the hit with resolution given essentially by the readout.

# Improve RPC (iRPC): RETURN and COAX prototypes

Solution RETURNConnect with a returnline within PCB (same impedance 45 Ω).Solution COAXConnect with coaxialcables. Cable impedance = 50 Ω.

### **RETURN** prototype better than COAX.

- more noise protection;
- less complicated construction process.





The Front-End Electronics Board (FEB) that hosts one PETIROC ASIC and the FPGA that includes the TDC and the schematics of the PETIROC ASIC [3]. (left) Photograph and dimensions of a prototype pickup-strip PCB. (right)

# **Electronic PETIROC2A: Pedestal, Injection, Noise**

The parameters of each channel (6-bit DAC) is adjusted so the pedestal S-curves of all channels are similar.



DAC unit

## **Description of the stand for tests of the prototype**



# H2 line: Study of time resolution



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# **GIF++: Study of rate capability**

14 TBq 137Cesium is used in GIF++ with different attenuation coefficient is used to obtain different gamma irradiation levels.

To test our chambers up to  $2 \text{ kHz} \cdot \text{cm}^{-2}$  rate needs to be seen in our chamber.



Fig.4 Floor plan of the GIF++ facility [4]







Loss of efficiency at a high rate is due partially to the DAQ dead time used to reload FEB channels after triggered by the noise.



- The linearity of the TOA time measurements and the time resolution of the TOA are verified on H2 line tests. Along strip resolution ~180ps.
- Measurements of the detector characteristics were carried out at the required noise on GIF tests 95% on 2kHz of background.

## Reference

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# **Thank for Your Attention!**

### Any questions:

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# **New Clustering algorithm**



# Noise

RateHR THR=61±10fC WINDOW=5µs COSMIC904:1237



RateLR THR=61±10fC WINDOW=5µs COSMIC904:1237



6.6 0

RateAND: THR=61±10fC WINDOW=5µs COSMIC904:1237

